

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Method and Control Circuit for Power Factor Correction																																		
<p>Application Number :</p> <p>Confirmation Number:</p> <p>First Named Applicant: Isaac Cohen</p> <p>Attorney Docket Number:</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: (5804950 or 6307361).pn</p> <p><u>Certification:</u> This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:</p> <p>That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>5</td><td>1</td><td>5804950</td><td>1998-09-08</td><td>Hwang et al.</td><td></td><td></td><td></td></tr><tr><td>5</td><td>2</td><td>6307361</td><td>2001-10-23</td><td>Yaakov et al.</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>Riley</td><td>Sept 05</td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	5	1	5804950	1998-09-08	Hwang et al.				5	2	6307361	2001-10-23	Yaakov et al.				Examiner Name	Date	Riley	Sept 05
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